

## **CURRICULUM VITAE**

**D. Swathi**

Plot No: 42

Krishna Nagar

Vijayawada -07

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### **CAREER OBJECTIVE:**

To be a part and participle of the creative field of learning, implementing and building. Thereby, trying to lay and follow the foot steps of learning and success.

### **ACADEMICS:**

<b>Course</b>	<b>Board Of Education</b>	<b>Year</b>	<b>%</b>
M.TECH M.C.E	P.V.P Siddhartha Institute of Science and Technology	2010-12	82%
B.TECH E.C.E	Koneru Lakshmaiah College of Engineering	2005-09	82.5 %
INTER M.P.C	Sir Chaitanya Day Scholars Mahila Kalasala	2003-05	87.3 %
SSC	Deepthi High School	2003	89 %

### **TEACHING EXPERIENCE:**

1. Worked as lecturer in Nimra College of Engineering and Technology from July 2009 to Sep 2011.
2. Trained the students as per GATE level in other institution.

### **PROFESSIONAL BODY MEMBERSHIPS:**

1. **ISTE** Life Member
2. **IE**

### **ACADEMIC AWARD:**

1. Won the award for securing the second position in academics from P.V.P Siddhartha Institute of Science and Technology.

**LIST OF PUBLICATIONS:**

***INTERNATIONAL JOURNALS:***

1. Published a paper on “BER performance of linear multiuser detectors in DS-CDMA”, International Journal of Scientific and Engineering Research, Volume 3, Issue 4, April-2012.
2. Published a paper on “Performance Analysis of Blind Adaptive Multi-user Detection in DS-CDMA”, International Journal of Engineering Research and Applications, Volume 2, Issue 5, September-October 2012.
3. published a paper on “Performance of Parallel Interference Cancellation Technique in Multistage Multiuser Detection for DS-CDMA System” , International Journal of Engineering Research and Applications, Vol. 2, Issue 3, May-Jun 2012, pp.1983-1987.
4. Published a paper entitled “Performance And Comparison Of Linear Multi User Detectors in DS – CDMA Using Chaotic Sequence” International Journal of Advances in Engineering Research, Vol.10, Issue :3, September, 2015.
5. Published a paper entitled “Efficient Data Transmission between Multiuser using CDMA Histogram Decoder” International Journal of Applied sciences, Engineering, Management, Vol.3, No :5, September, 2014.
6. published a paper entitled “Energy Efficiency for MIMO-OFDM using Iterative Water Filling Algorithm” International Journal of Research in Computer and Communication Technology, Vol:4, Issue No:9, P.P:719-722, September, 2015.
7. published a paper entitled “Performance Analysis of Adaptive MMSE in DS-CDMA Systems” Proceedings of National Convention on Recent Advances in Microwaves, Antennas and Communications 1st -2 nd July 2016, Organized by PVPSIT and The Institution of Engineers India
8. Published a paper entitled “Analysis of Margin Adoptive Resource Allocation in MIMO Systems” - International Journal of Wireless Communication, Vol:9, No:6, July, 2017 PP:-119-122, ISSN:0974:9756.
9. Published a paper entitled Home Automation based on IOT using Google Assistant – IJARTET, Issue:1; vol:6; January - 2019.
10. published a paper entitled Smart indoor ambiance monitoring and controlling system using IOT - IJARTET, Issue:1; vol:6; January – 2019

11. published a paper entitled Arduino Based Smart Indoor Ambiance Monitoring and Controlling System - International Journal of Engineering Research and Applications (IJERA), ISSN :2248-9622 Vol. 9, Issue 4 (Series -IV); pp 29-32; April – 2019.
12. Published a paper entitled Heart-Rate Monitoring System Using Node MCU -JETIR, ISSN-2349-5162 April 2019, Volume 6, Issue 4.

#### ***INTERNATIONAL CONFERENCES:***

1. Attended a International conference on “Signal Processing and Communication Engineering Systems Spaces-2015 ” at KL University in association with IEEE on 2-3 January 2015.
2. Presented a paper entitled “Interference reduction in DS – CDMA system with blind detector”- International Multi Disciplinary conference on Knowledge sharing and Technological Advancements and Industrial Development at PVPSIT on 30th March, 2018.

#### ***NATIONAL CONFERENCES:***

1. **D.SWATHI** presented a paper on “comparison of liner multiuser detectors in DS-CDMA using chaotic sequence “on national conference on advanced signal processing applications held on 20-21<sup>st</sup> December 2013 organized by signal processing research group, KL University .

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#### ***WORKSHOPS CONDUCTED:***

1. Acted as a Resource person in the joint training program conducted with the collaboration of Effelectronics Systems Pvt Ltd on “PCB Design and making” held on 14-15 November 2014.
2. A Workshop was conducted on “PCB Design” for the students of Government Polytechnique College from 9th November, 2016 to 15th November, 2016
3. A one day Training Programme was organized on “PCB Designing & Soldering practice” on 3rd March, 2018 for II B.Tech. in association with Effronics Pvt. Ltd., under IETE Students Forum.
4. A guest lecture was conducted on Latest Technology and Advances in U.S.A on 24th December, 2018

#### ***WORKSHOPS-GUEST LECTURES-SEMINARS ATTENDED:***

1. Participated in the two day national level workshop on “**importance of free and open source software as a major role player in software industry’s growth**” at KLCE-2008.
2. Attended the two day national seminar on “**smart antenna in fourth generation wireless systems**” at VRSEC-2011.
3. Participated in one week training programme on “**Instructional Design And Delivery Systems**” conducted at PVPSIT by NITTTR (NATIONAL INSTITUTE OF TECHNICAL TEACHERS TRAINING & RESEARCH, CHENNAI from 8<sup>th</sup> to 12<sup>th</sup> July 2013.
4. Participated in the three day training programme organized by the department of ECE , Prasad V Potluri Siddhartha Institute of Technology ,Kanuru, Vijayawada on “**Advanced Microwave & Communication Engineering Equipment**” held from 23<sup>rd</sup> to 25<sup>th</sup> of July ,2013.
5. Participated in National Workshop on “**Computerized Tomography-Medical & Industrial Applications (CTMIA)**”,organized by signal processing research group (SPRG) Dept of ECE,KL University, Vaddeswaram, Guntur ,A.P during 10<sup>th</sup> August 2013.
6. **D.SWATHI** has participated in AICTE sponsored National seminar on “Mathematical Challenges on Medical Image Processing” held on 27-28<sup>th</sup> December, 2013.
7. Participated in one day training programme on “**Effective Teaching Methodologies**” organized by ISTE CHAPTER - Prasad V Potluri Siddhartha Institute of Technology ,Kanuru, Vijayawada on 26<sup>th</sup> July ,2014.
8. Participated in a two week ISTE workshop on “**Signals & Systems**” conducted by Indian Institute of Technology Kharagpur from 2<sup>nd</sup> to 12<sup>th</sup> January, 2014.this workshop was held under the National Mission on Educational through ICT (MHRD).
9. Attended a one week workshop on “**PCB Designing –Hands on Practice**” which is organized by Effectronics Systems Pvt Ltd from 30<sup>th</sup> June to 5<sup>th</sup> July 2014.
10. Participated in the **International Conference SPACES-2015**, held at Department of ECE, KL University, and Andhra Pradesh in association with **IEEE** on 2<sup>nd</sup> to 3<sup>rd</sup> January 2015.
11. Attended a five day Faculty Development Programme on “**Advanced Digital Signal Processing and Applications (ADSP-A)**” at VRSEC, Vijayawada from 9<sup>th</sup> September, 2015 to 13<sup>th</sup> September 2015.

12. Attended a one week Faculty Development Programme on “**Industrial Automation**” under the National Mission on Education through ICT from 5th October 2015 to 9th October 2015 organized by National Institute of Technical Teachers Training and Research (NITTTR), Chandigarh at PVPSIT conducted by Mechanical Engineering
13. Attended “**A mind Training Programme**” organized by International Youth Fellowship of South Coreans at PVPSIT, on 18th December, 2015.
14. Attended a three day training program was conducted on **NI My DAQ** during 16th – 18th July, 2018.
15. Attended one week ICT based Faculty Development Program on **VLSI Design** conducted by NITTTR Chandigarh, organized by dept. of ECE during 24th – 28th September, 2018 at PVPSIT knowledge Centre.
16. Attended seminar on **IPR7 Patents** on 29th September, 2018 conducted by ISTE faculty chapter in association with IQAC.
17. Attended a two day hands on workshop on **PCB Design and Prototype development** organized by the department of Electronics and Communication Engineering, Vellore Institute of Technology, A.P. during 8th - 9 th December, 2018
18. attended a three day training on **Programming Xilinx Zynq SoCS** using MATLAB and Simulink conducted by MATHWORKS at Sri Vasavi Engineering College, Tadepalligudem during 25th - 27th February, 2019.

**SUBJECTS TAUGHT/LABS HANDLED:**

1. Microprocessors And Interfacing
2. Electronic Measurements and Instrumentation
3. Transmission lines and Waveguides
4. Electronic Devices and Circuits
5. Microwave Engineering
6. Satellite Communication
7. Digital Signal Processing Lab
8. Electronic Devices and Circuits Lab
9. Electro Magnetic Fields

**SELF PROFILE:**

Name	:	Ms.D.Swathi
Date of Birth	:	25 <sup>th</sup> September, 1988.
Father’s Name	:	Mr. Gandhi.
Languages Known	:	Telugu & English

Marital Status : Unmarried  
Present Address : Flat no: FF2 plot no 42  
Krishna Nagar  
Old check post center Vijayawada.  
Nationality : Indian  
Contact no : 9640934593  
E-Mail Address : dswathi289@gmail.com

**DECLARATION:**

I hereby declare what I have said above is true and try to implement more than what I have penned down.

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**Date :**

**Place : VIJAYAWADA**

**(DASI SWATHI)**