

**PVP SIDDHARTHA INSTITUTE OF TECHNOLOGY, KANURU, VIJAYAWADA**  
(AUTONOMOUS)  
**INFORMATION TECHNOLOGY**

**COMPILER DESIGN**

|   |          |                                 |       |                      |                                   |
|---|----------|---------------------------------|-------|----------------------|-----------------------------------|
| <b>Course Code</b>                      | 19IT3601 | <b>Year</b>                     | III   | <b>Semester</b>      | II                                |
| <b>Course Category</b>                  | PC       | <b>Branch</b>                   | IT    | <b>Course Type</b>   | Theory                            |
| <b>Credits</b>                          | 3        | <b>L-T-P</b>                    | 3-0-0 | <b>Prerequisites</b> | Formal Language & Automata Theory |
| <b>Continuous Internal Evaluation :</b> | 30       | <b>Semester End Evaluation:</b> | 70    | <b>Total Marks:</b>  | 100                               |

| <b>Course Outcomes</b>   |   | <b>Blooms Taxonomy Level</b> |
|--|---|------------------------------|
| <b>Upon Successful completion of course, the student will be able to</b> |   |                              |
| CO1  | Understand about language processors and its phases.  | L2                           |
| CO2  | Demonstrate about scanning of tokens and perform the syntax analysis by using parsing techniques                            | L3                           |
| CO3  | Perform Symantec analysis using attribute grammar and compare different memory management techniques in runtime environment | L3                           |
| CO4  | Ascertain optimization techniques for intermediate code forms and code generation   | L3                           |

| <b>Contribution of Course Outcomes towards achievement of Program Outcomes &amp; Strength of correlations (H:High, M: Medium, L:Low)</b> |     |     |     |     |     |     |     |     |     |      |      |      |      |      |
|--|-----|-----|-----|-----|-----|-----|-----|-----|-----|------|------|------|------|------|
|  | PO1 | PO2 | PO3 | PO4 | PO5 | PO6 | PO7 | PO8 | PO9 | PO10 | PO11 | PO12 | PSO1 | PSO2 |
| <b>CO1</b>   | 3   | 2   |     |     |     |     |     |     |     |      |      |      | 2    | 1    |
| <b>CO2</b>   | 3   | 3   | 2   | 1   | 3   |     |     |     |     |      |      | 1    | 2    | 1    |
| <b>CO3</b>   | 2   | 3   |     | 1   | 3   |     |     |     |     |      |      | 1    | 2    | 2    |
| <b>CO4</b>   | 2   | 3   | 2   |     | 3   |     |     |     |     |      |      | 1    | 3    | 3    |

| Syllabus |  |               |
|----------|--|---------------|
| Unit No  | Contents   | Mappe d CO    |
| I        | <p><b>Overview of language processing:</b> preprocessors – compiler – assembler – Linkers &amp; loaders, difference between compiler and interpreter-structure of a compiler –phases of a compiler.</p> <p><b>Lexical Analysis:</b> Role of Lexical Analysis – Input Buffering – Specification of Tokens – Recognition of Token – The Lexical Analyzer Generator Lex.</p>  | CO1           |
| II       | <p><b>Syntax Analysis:</b> Role of a parser – Context Free Grammar – Top Down Parsing – Recursive Descent Parsing — Non recursive Predictive Parsing- FIRST and FOLLOW – LL(1) Grammar – Error Recovery in Predictive Parsing.</p>   | CO1, CO2      |
| III      | <p><b>Bottom up Parsing:</b> Reductions – Handle Pruning - Shift Reduce Parsing – Introduction to simple LR – Why LR Parsers – Model of an LR Parsers — Construction of SLR Tables.</p> <p><b>More powerful LR parsers:</b> Construction of CLR (1) - LALR Parsing tables.</p>   | CO1, CO3      |
| IV       | <p><b>Runtime Environment:</b> Storage organization - Stack allocation – Static allocation – Heap management - Parameter passing mechanisms.</p> <p><b>Intermediate code:</b> DAG - Three address code – Quadruples - Triples - Indirect Triples.</p>  | CO1, CO3, CO4 |
| V        | <p><b>Basic Blocks:</b> DAG representation of Block. Machine independent code optimization - Common sub expression elimination - Constant folding - Copy propagation -Dead code elimination - Strength reduction - Loop optimization.</p> <p><b>Machine dependent code optimization:</b> Peephole optimization – Register allocation - Instruction scheduling - Inter Procedural Optimization - Garbage collection via reference counting.</p> | CO1, CO4      |

| Learning Resources  |
|---|
| <b>Text Books</b>   |
| <ol style="list-style-type: none"> <li>1. Compilers: Principles, Techniques and Tools: 2nd Edition, Alfred V. Aho, Monica S. Lam, Ravi Sethi, Jeffrey D. Ulman; 2nd Edition, Pearson Education.</li> <li>2. Modern Compiler Implementation in C- Andrew N. Appel, Cambridge University Press, First edition.</li> </ol>   |
| <b>References</b>   |
| <ol style="list-style-type: none"> <li>1. lex &amp; yacc – John R. Levine, Tony Mason, Doug Brown, O'reilly, 2<sup>nd</sup> edition, 2017.</li> <li>2. Modern Compiler Design- Dick Grune, Henry E. Bal, Criel T. H. Jacobs, Wileyream tech, 2012.</li> <li>3. Engineering a Compiler-Cooper &amp; Linda, Elsevier, Third edition.</li> <li>4. Compiler Construction, Loudon, Thomson, First edition.</li> <li>5. Principles of compiler design, V. Raghavan, 2<sup>nd</sup> edition, TMH, 2011.</li> </ol> |
| <b>E-Resources and other Digital Material</b>   |
| <ol style="list-style-type: none"> <li>1. <a href="http://www.nptel.iitm.ac.in/downloads/106108052/">http://www.nptel.iitm.ac.in/downloads/106108052/</a></li> </ol>  |